

**AMENDMENTS TO THE ABSTRACT**

Please amend the abstract as set forth below in marked-up form.

In a CMOS image pickup device in which the signal corresponding to the accumulated charge amount of each pixel is output to horizontal signal lines wired on a row basis, for example, two vertical selection transistors are provided for every horizontal signal lines, and two vertical signal lines and two vertical scan circuits are provided for every horizontal signal line, thereby separately outputting signals which are different in accumulation time and obtained by arbitrarily dividing 1 field into any number of parts on the basis of integer times of 1H.